# CSCE-312 | Fall 2020 | Project1

## Building Boolean Logic (Gates and Basic Functions)

|  |
| --- |
| **Ecampus Submission: 100 points**  **Due Date:** **Submit on eCampus by Monday, Sep 7th, 11:59 PM**  **Grading**  **Project Submission [100%]:** **Submission logistics will be posted on Teams in the #Project1 channel** You will be graded for correctness of the chips (hdl) you have designed and coded. TA’s will run tests on all the HDL codes downloaded from your eCampus using Nand2tetris software (Hardware Simulator). So, make sure to test and verify your codes before finally submitting on eCampus.  ***Rubric:***Each chip needs to pass all its test cases to get full credit, else you will receive a **0 point** on that chip. We may, at our discretion, choose to give partial credit for a chip if it seems like a large number of people are failing it.  **Deliverables & Submission**  You need to turn in **the completed HDL files** for **all the chips** implemented. In addition, you need to turn in the completed **tst and cmp files** for the **Xnor and 8-to-3 priority encoder** chips. Put your **full name** in the introductory comment present in each HDL code. Use relevant code comments and indentation. Also, include this **cover sheet** with your signature below. Zip all the required HDL files and the signed cover sheet into a compressed file ***FirstName-LastName-UIN.zip*** . Submit this zip file on eCampus.  **Late Submission Policy:** Refer to the Syllabus |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | **Full Name:** | Yuhao Ye | **Section:** | CSCE312 599 | **UIN:** | 529006730 |   **Any assignment turned in without a fully completed cover page will NOT BE GRADED.**  Please list all below all sources (people, books, web pages, etc) consulted regarding this assignment:  CSCE 312 Students Other People Printed Material Web Material (URL) Other  1. 1.Lab video 1.Lab document 1.https://www.nand2tetris.org/hdl-survival-guide .  2. 2. 2.HDL Survival Guide 2. https://www.electronics-tutorials.ws/combination/2.  3. 3. 3. 3. 3.  Please consult the Aggie Honor System Office for additional information regarding academic misconduct – it is your responsibility to understand what constitutes academic misconduct and to ensure that you do not commit it.  I certify that I have listed above all the sources that I consulted regarding this assignment, and that I have not received nor given any assistance that is contrary to the letter or the spirit of the collaboration guidelines for this assignment.   |  |  | | --- | --- | | **eCampus Submission Date:** | 09/06/2020 |  |  |  | | --- | --- | | **Printed Name (in lieu of a signature):** | Yuhao Ye | |